

Docket No.: M4065.0422/P422  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Howard E. Rhodes

Application No.: 09/941,546

Group Art Unit: 2811

Filed: August 30, 2001

Examiner: Not Yet Assigned

For: CMOS IMAGER AND METHOD OF  
FORMATION

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

Submitted herewith is one set (nine sheets, fifteen figures) of formal drawings for filing in the above-identified Patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: November 19, 2001

Respectfully submitted,

By   
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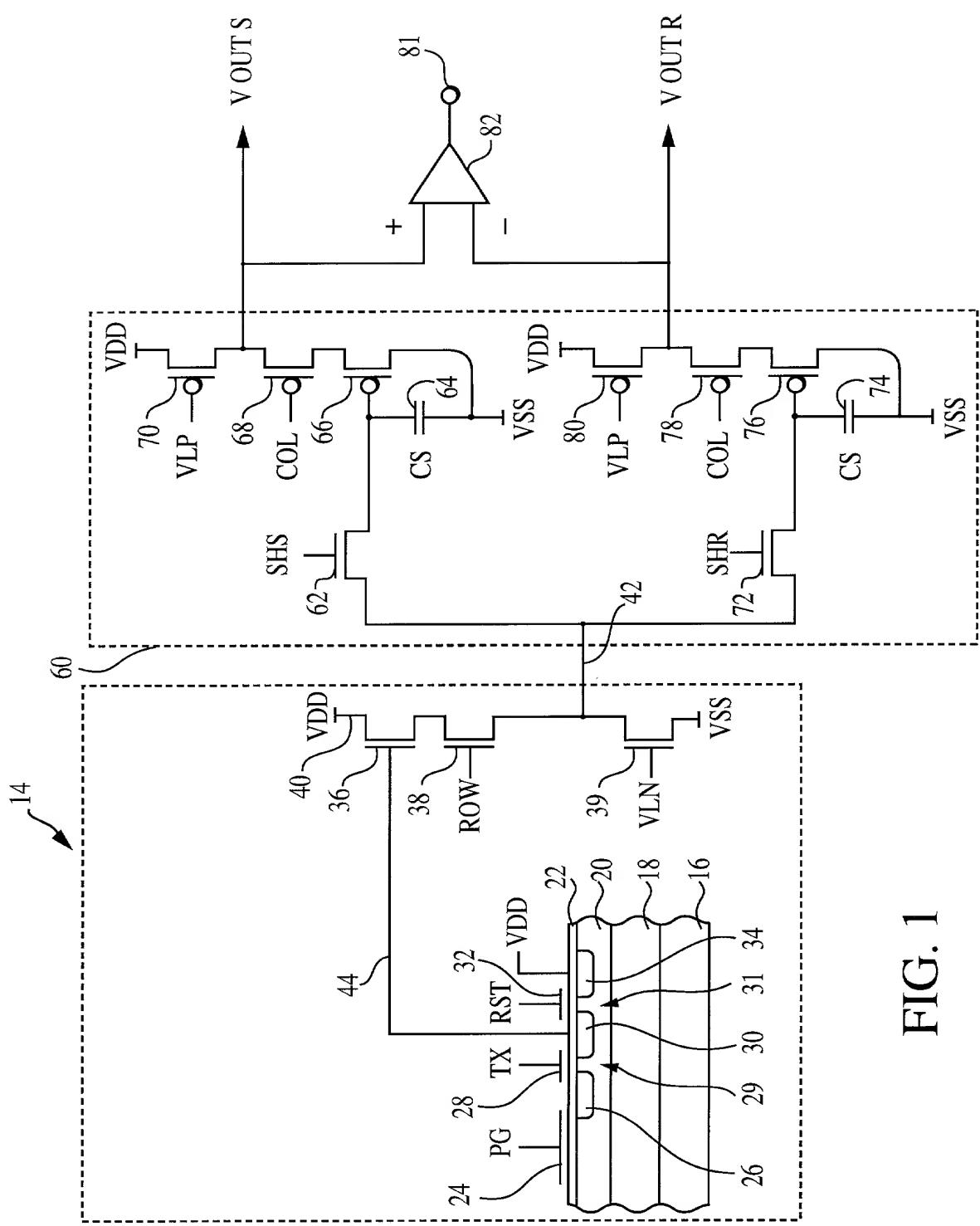


FIG. 1

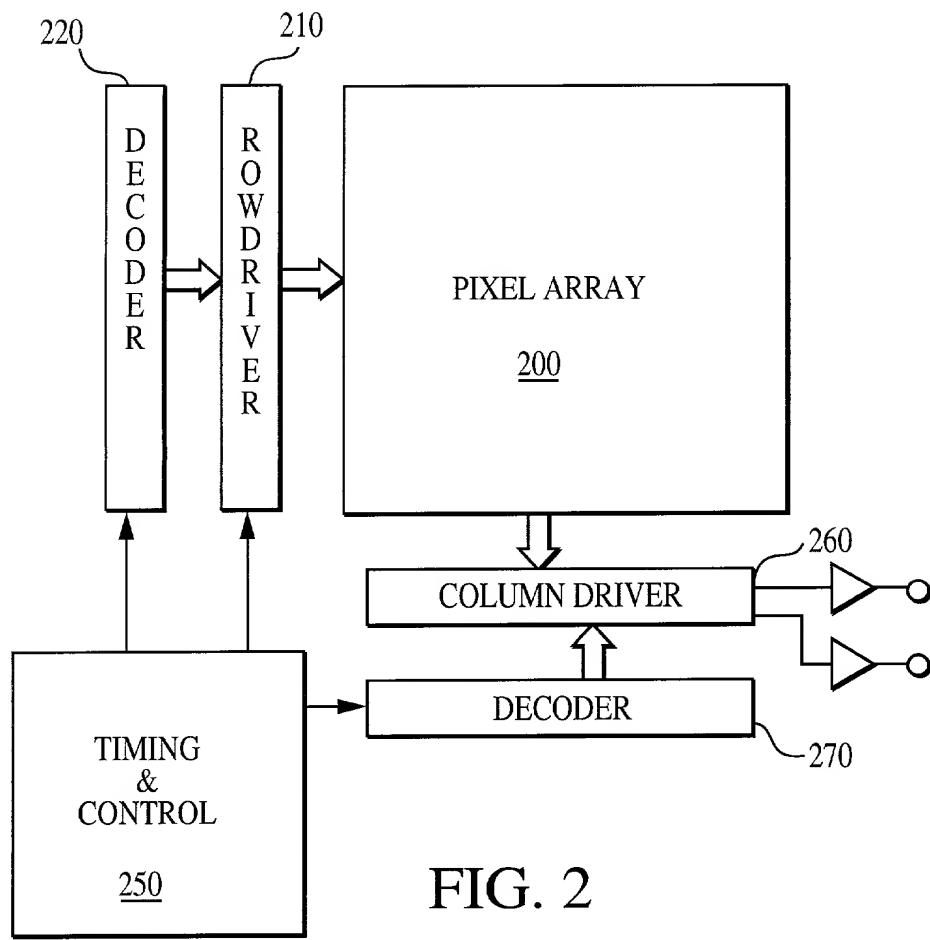


FIG. 2

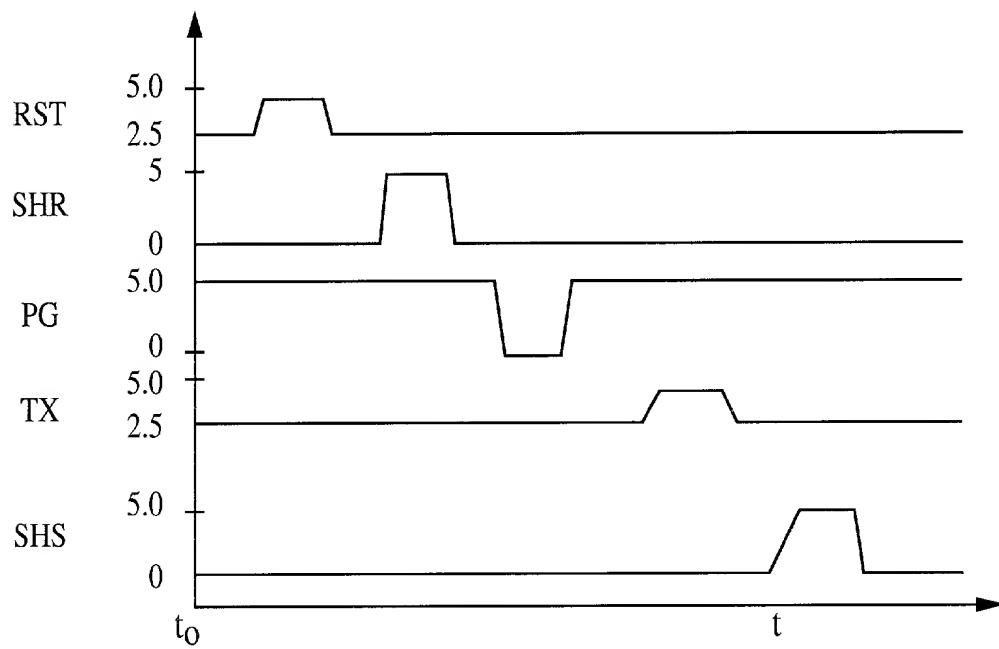
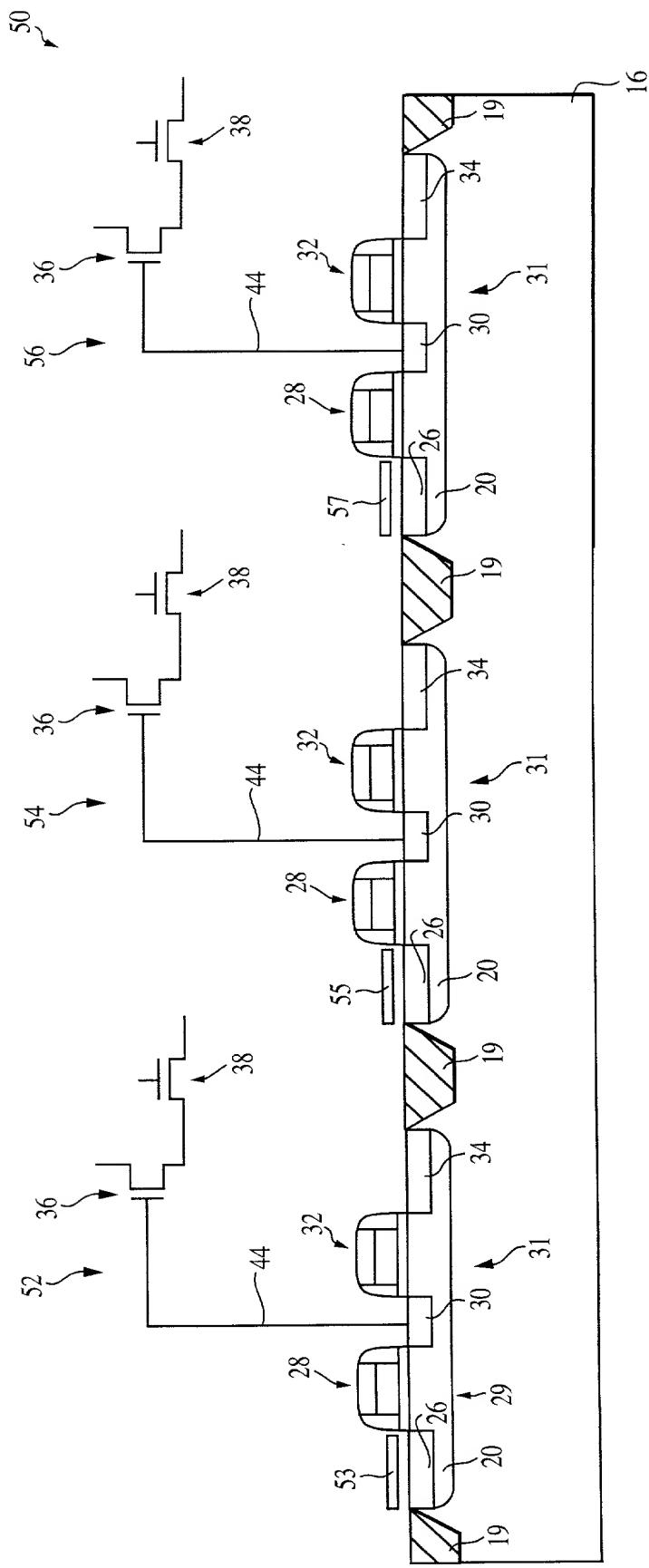


FIG. 3

FIG. 4



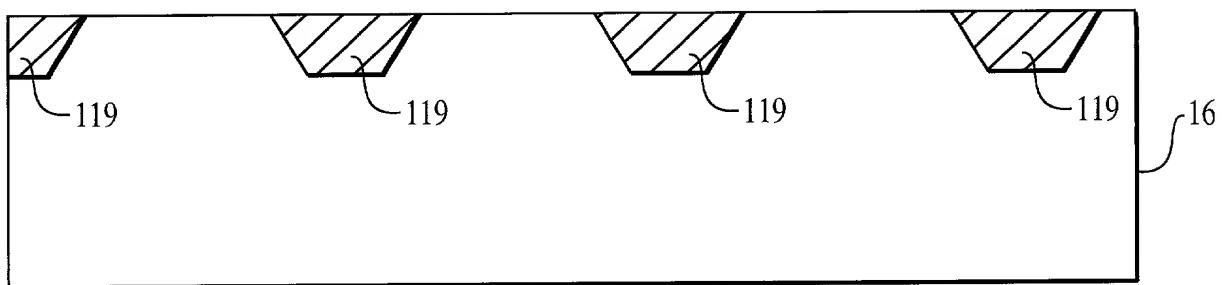


FIG. 5

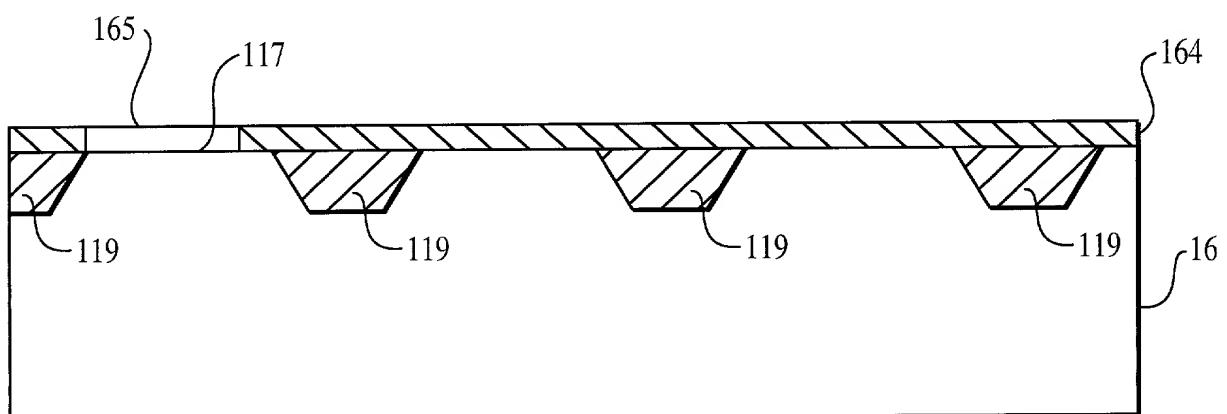


FIG. 6

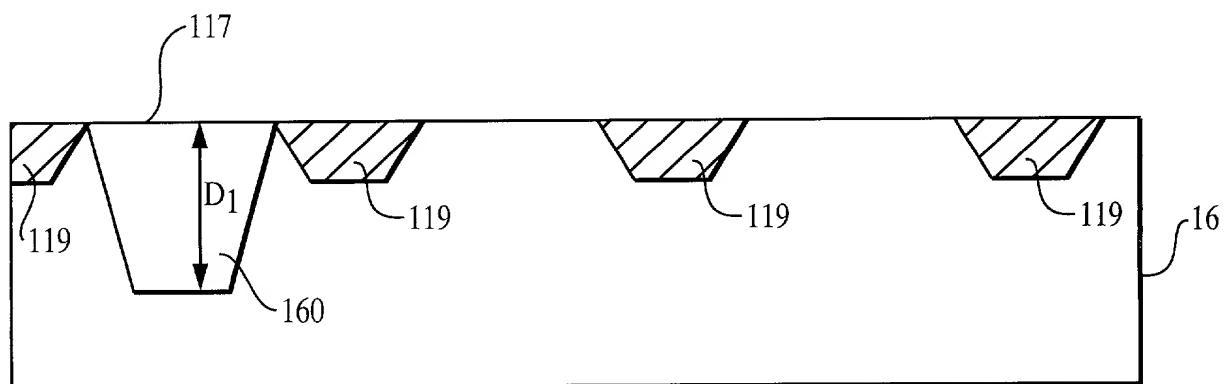


FIG. 7

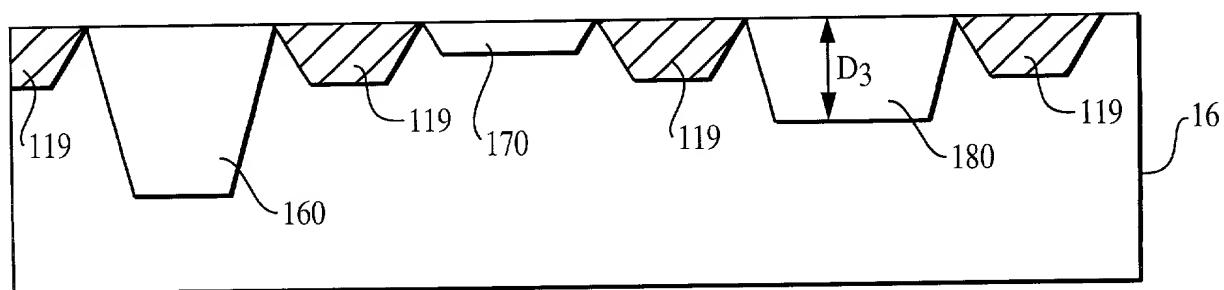
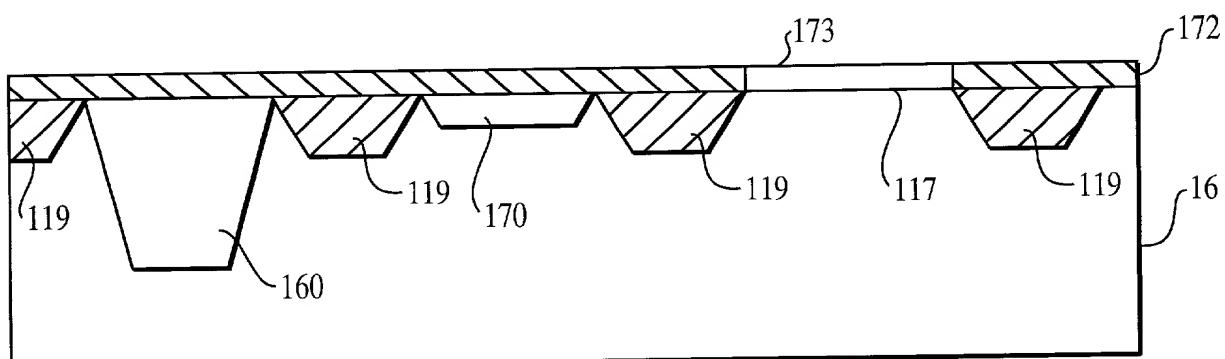
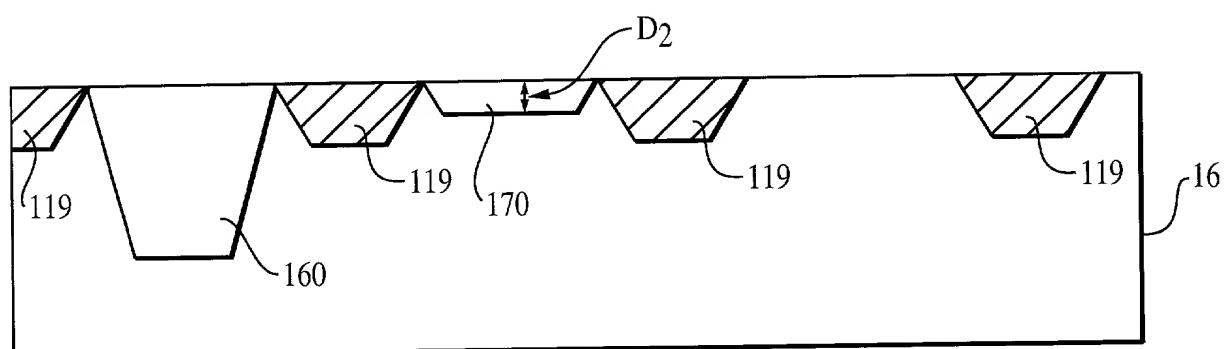
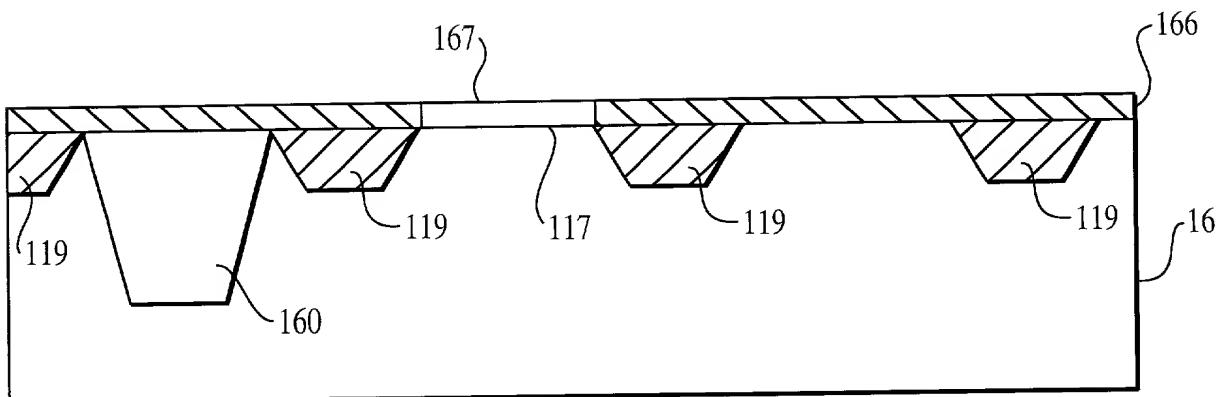
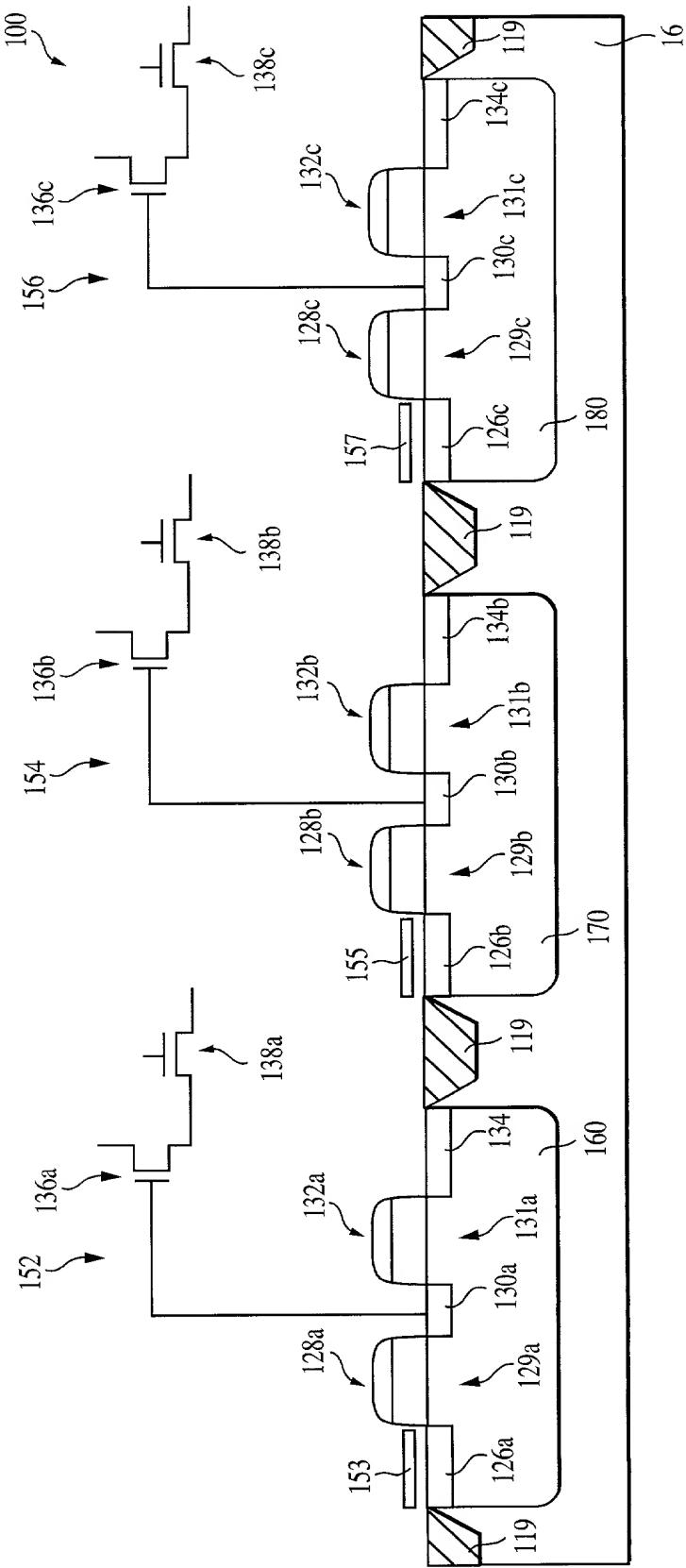


FIG. 12



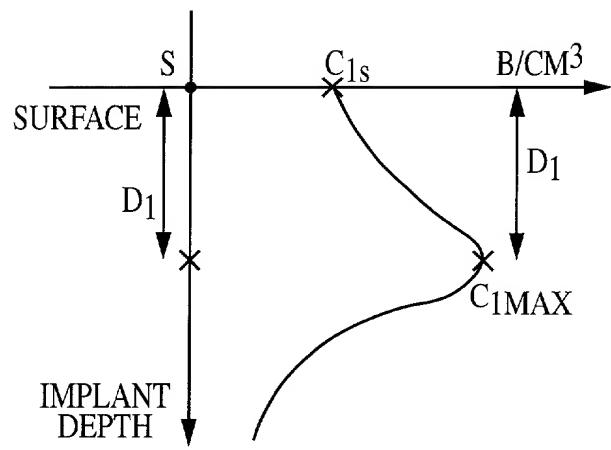


FIG. 13a

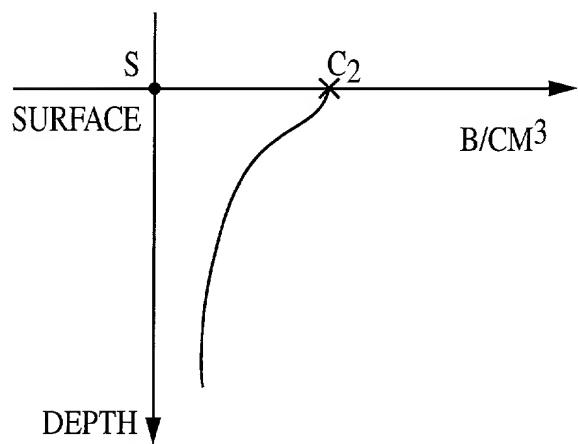


FIG. 13b

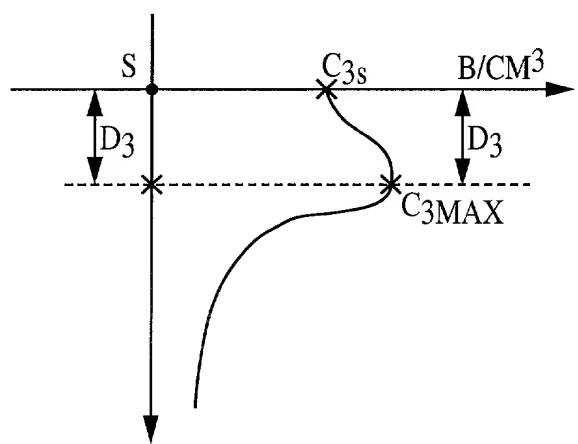
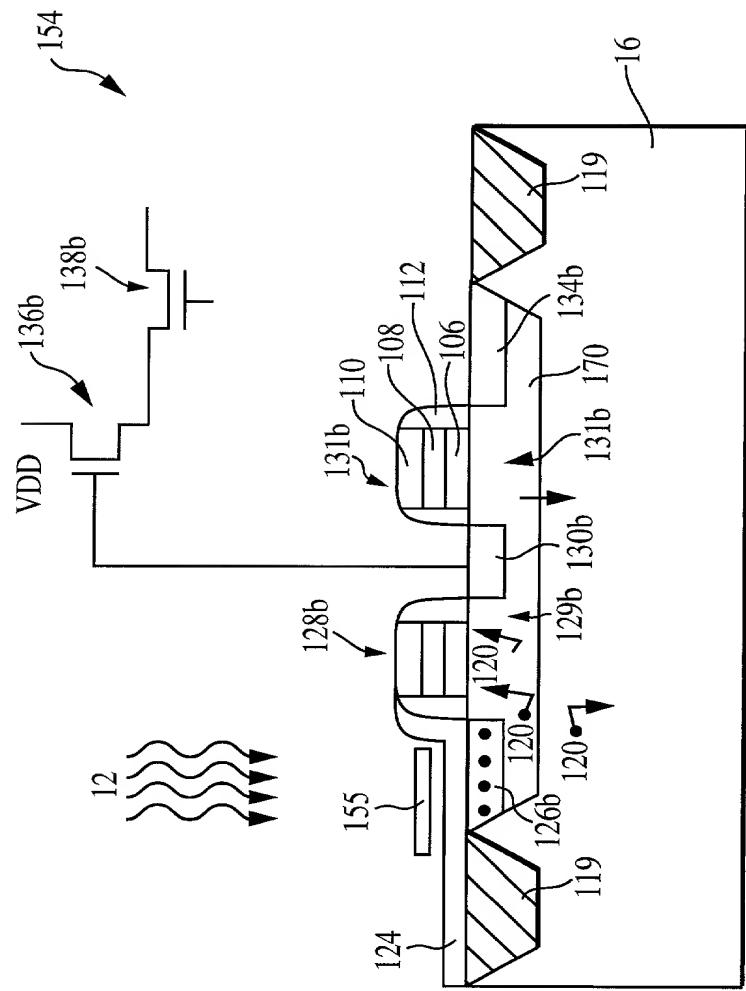


FIG. 13c

FIG. 14



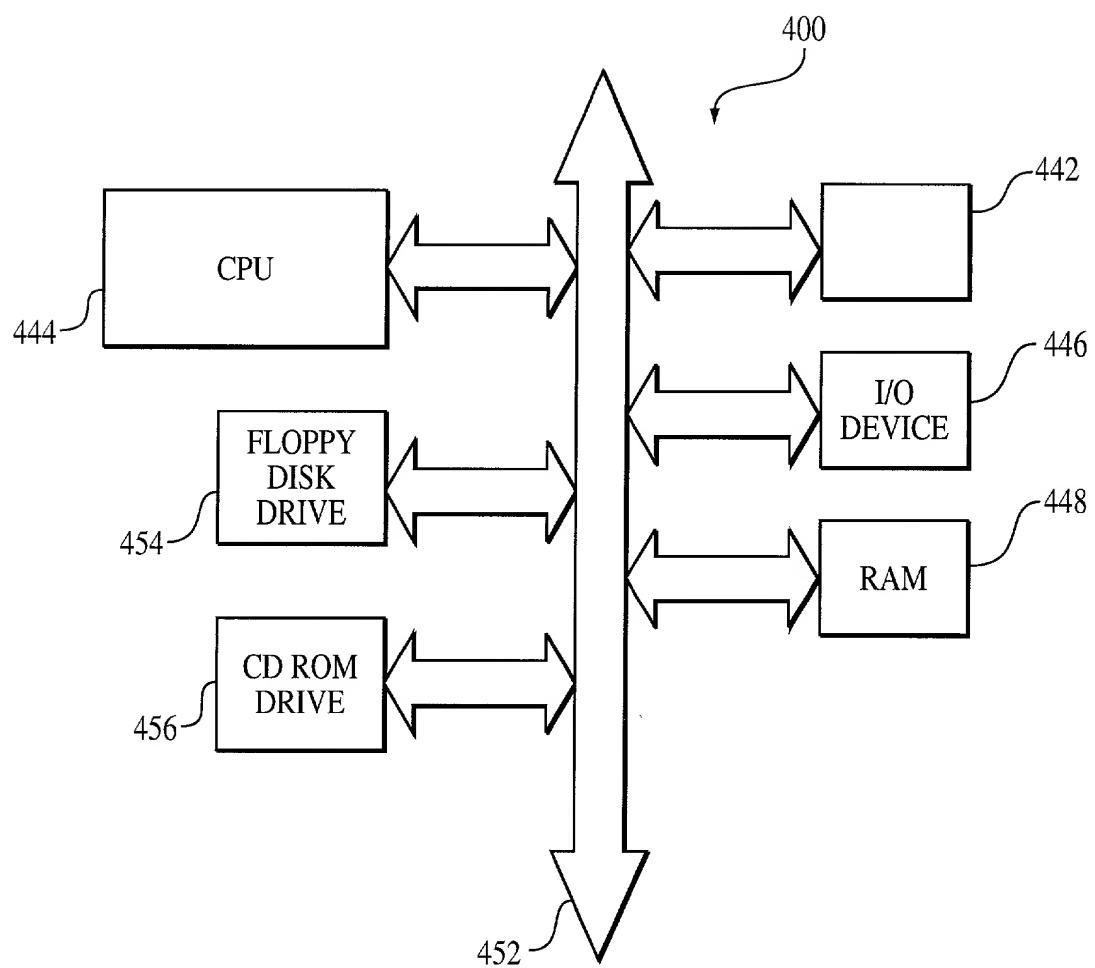


FIG. 15